



### **650V N-Channel MOSFET**

Voltage

650 V

Current

10 A

#### **Features**

- R<sub>DS(ON)</sub>, V<sub>GS</sub>@10V,I<sub>D</sub>@5A<1Ω
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std. (Halogen Free)

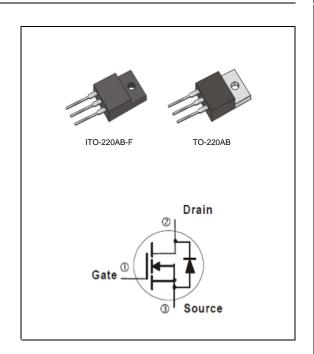
### **Mechanical Data**

• Case: TO-220AB, ITO-220AB-F Package

• Terminals : Solderable per MIL-STD-750, Method 2026

TO-220AB Approx. Weight: 0.067 ounces, 1.89 grams

• ITO-220AB-F Approx. Weight: 0.068 ounces, 2 grams



## Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER		SYMBOL	TO-220AB	ITO-220AB-F	UNITS
Drain-Source Voltage		V <sub>DS</sub>	650	V	
Gate-Source Voltage		$V_{GS}$	<u>+</u> 30		V
Continuous Drain Current		I <sub>D</sub>	10		А
Pulsed Drain Current		I <sub>DM</sub>	40		Α
Single Pulse Avalanche Energy (Note 1)		E <sub>AS</sub>	608		mJ
Power Dissipation	T <sub>C</sub> =25°C	P <sub>D</sub>	156	50	W
	Derate above 25°C		1.25	0.4	W/°C
Operating Junction and		$T_J$ , $T_{STG}$	-55~150		°C
Storage Temperature Range					C
Typical Thermal resistance					
- Junction to Case		$R_{ heta JC}$	0.8	2.5	°C/W
- Junction to Ambient		$R_{\theta JA}$	62.5	120	

Limited only By Maximum Junction Temperature





# **Electrical Characteristics** (T<sub>A</sub>=25 °C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V,I <sub>D</sub> =250uA	650	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=250uA$	2	2.8	4	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V,I <sub>D</sub> =5A	-	0.88	1.0	Ω
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =650V,V <sub>GS</sub> =0V	-	0.03	1.0	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = <u>+</u> 30V,V <sub>DS</sub> =0V	-	<u>+</u> 10	<u>+</u> 100	nA
Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> =10A,V <sub>GS</sub> =0V	-	0.9	1.4	V
Dynamic (Note 4)						
Total Gate Charge	$Q_g$	$V_{DS}=520V, I_{D}=10A,$		20	-	nC
Gate-Source Charge	$Q_{gs}$			8	-	
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> =10V <sup>(Note 2,3)</sup>	-	7	-	1
Input Capacitance	Ciss	)/ OF)/ )/ O)/	-	1200	-	pF
Output Capacitance	Coss	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V,	-	140	-	
Reverse Transfer Capacitance	Crss	f=1.0MHZ		4	-	
Switching						
Turn-On Delay Time	td <sub>(on)</sub>	V <sub>DD</sub> =325V, I <sub>D</sub> =10A,	-	40	-	ns
Turn-On Rise Time	t <sub>r</sub>	$R_G=25\Omega$ (Note 2,3)	-	74	-	
Turn-Off Delay Time	td <sub>(off)</sub>		-	52	-	
Turn-Off Fall Time	t <sub>f</sub>		-	35	-	
Drain-Source Diode						
Maximum Continuous Drain-Source				- 10	40	А
Diode Forward Current	I <sub>S</sub>		-		10	
Maximum Pulsed Drain-Source					40	A
Diode Forward Current			-	-	40	A
Reverse Recovery Time	trr	V <sub>GS</sub> =0V, I <sub>S</sub> =10A	-	570	-	ns
Reverse Recovery Charge	Qrr	dI <sub>F</sub> / dt=100A/us (Note 2)	-	4.7	-	uC

#### NOTES:

- 1. L=30mH,  $I_{AS}$ =6.2A,  $V_{DD}$ =50V,  $R_{G}$ =25 ohm, Starting  $T_{J}$ =25 $^{\circ}$ C
- 2. Pulse width<300us, Duty cycle<2%
- 3. Essentially independent of operating temperature typical characteristics.
- 4. Guaranteed by design, not subject to production testing





### **TYPICAL CHARACTERISTIC CURVES**

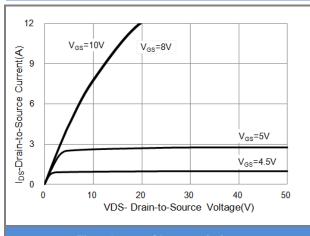
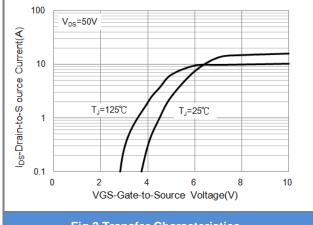


Fig.1 Output Characteristics



**Fig.2 Transfer Characteristics** 

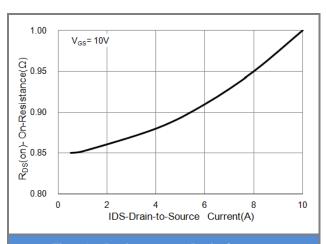


Fig.3 On-Resistance vs. Drain Current

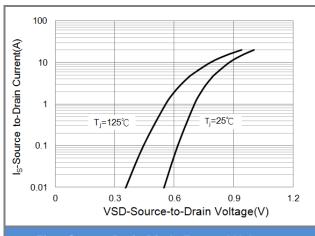
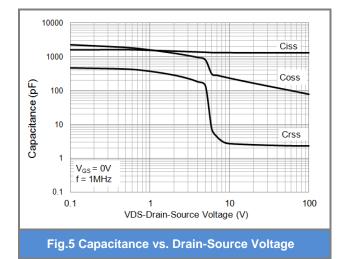
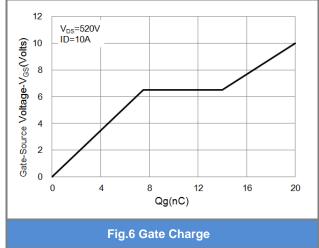


Fig.4 Source-Drain Diode Forward Voltage









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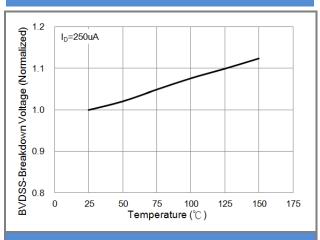


Fig.7 BV<sub>DSS</sub> vs. Junction Temperature

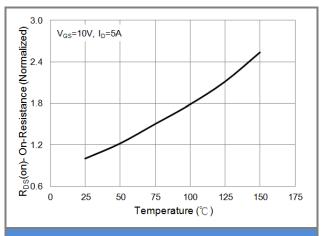


Fig.8 On-Resistance vs. Junction Temperature

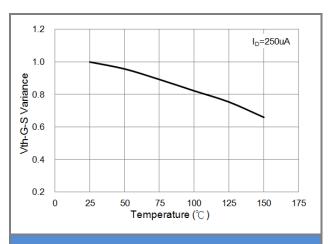


Fig.9 Threshold Voltage Variation with Temperature

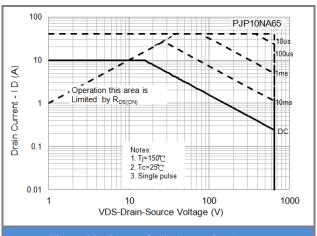


Fig.10 Maximum Safe Operating Area

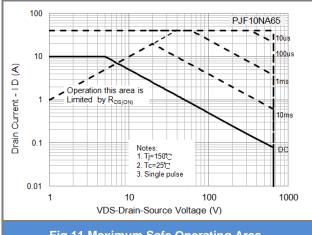


Fig.11 Maximum Safe Operating Area





### **TYPICAL CHARACTERISTIC CURVES**

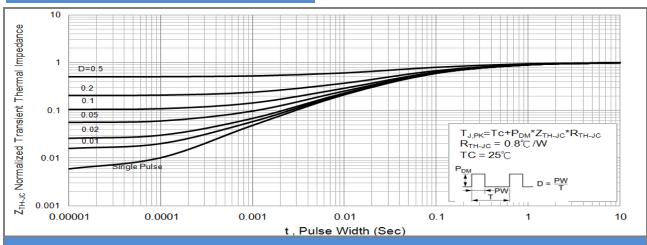


Fig.13 PJP10NA65 Normalized Transient Thermal Impedance vs. Pulse Width

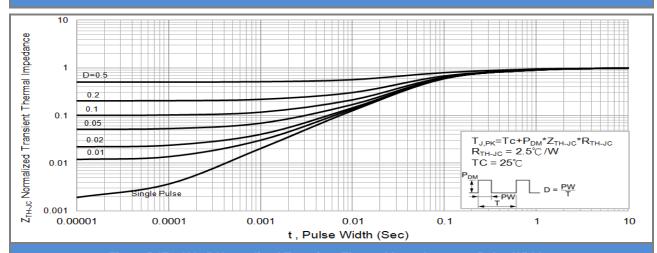
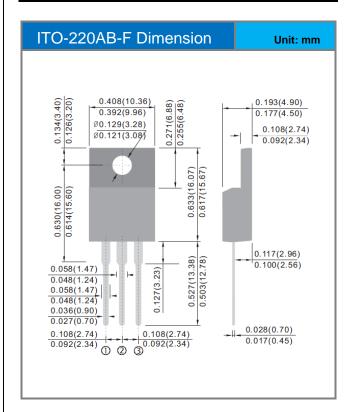


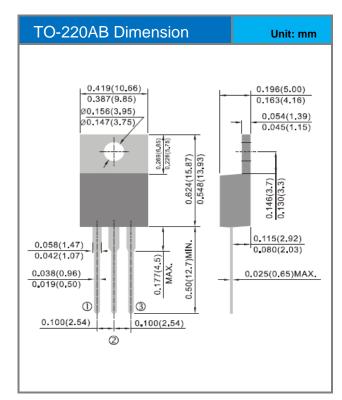
Fig.14 PJF10NA65 Normalized Transient Thermal Impedance vs. Pulse Width





### **Packaging Information**









### PART NO PACKING CODE VERSION

Part No Packing Code	Package Type	Packing type	Marking	Version
PJP10NA65_T0_00001	TO-220AB	50pcs / Tube	P10NA65	Halogen free
PJF10NA65_T0_00001	ITO-220AB-F	50pcs / Tube	F10NA65	Halogen free





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